

What is claimed is:

1. A plasma display apparatus including:

(A) a panel section including:

(A-1) a first board having a plurality of scanning electrodes and
5 sustain electrodes in pairs; and

(A-2) a second board having data electrodes which cross the
scanning electrodes and the sustain electrodes, and faced the first board; and

(B) a driver for outputting a driving voltage for driving the panel
section,

10 the plasma display apparatus comprising:

(a) a sustaining period when a sustaining pulse is alternately applied
to the scanning electrodes and the sustain electrodes for keeping discharge; and

(b) an erasing period when a ramp voltage pulse whose polarity
differs from polarity of the sustaining pulse is applied to an electrode, which
15 differs from an electrode where a last pulse of the sustaining pulse is applied.

2. The plasma display apparatus of claim 1,

wherein the last pulse of the sustaining pulse is positive, and

a minimum voltage V_{nr} of the ramp voltage pulse, which is applied in
20 the erasing period, has a following relation for a discharge-starting voltage V_{f1}
between an electrode where the ramp voltage pulse is applied and one of the
data electrodes.

$$-(V_{f1} - 60) \leq V_{nr} \leq -30 \quad (\text{V of units})$$

25 3. A plasma display apparatus including:

(A) a panel section including:

(A-1) a first board having a plurality of scanning electrodes and

sustain electrodes in pairs; and

(A-2) a second board having data electrodes which cross the scanning electrodes and the sustain electrodes, and faced the first board; and

(B) a driver for outputting a driving voltage for driving the panel
5 section,

the plasma display apparatus comprising:

a sustaining period when a sustaining pulse is alternately applied to the scanning electrodes and the sustain electrodes for keeping discharge,

wherein a voltage V_{sh} of a last pulse of the sustaining pulse has a
10 following relation for a voltage V_{st} of the sustaining pulse before the last pulse and a discharge-starting voltage V_{f2} between one of the scanning electrodes and one of the sustain electrodes.

$$V_{st} \leq V_{sh} \langle V_{f2}$$

15 4. The plasma display apparatus of claim 3,

wherein the voltage V_{sh} of the last pulse of the sustaining pulse has a following relation for the discharge-starting voltage V_{f2} between the scanning electrode and the sustain electrode.

$$(V_{f2} - 50) \leq V_{sh} \langle (V_{f2} - 30) \quad (\text{V of units})$$

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5. A plasma display apparatus including:

(A) a panel section including:

(A-1) a first board having a plurality of scanning electrodes and sustain electrodes in pairs; and

25 (A-2) a second board having data electrodes which cross the scanning electrodes and the sustain electrodes, and faced the first board; and

(B) a driver for outputting a driving voltage for driving the panel

section,

the plasma display apparatus comprising:

a sustaining period when a sustaining pulse is alternately applied to the scanning electrodes and the sustain electrodes for keeping discharge,

5 wherein a pulse width ts_2 of a last pulse of the sustaining pulse is wider than a pulse width ts_1 of the sustaining pulse before the last pulse.

6. The plasma display apparatus of claim 5,

 wherein the pulse width ts_2 of the last pulse of the sustaining pulse has
10 a following relation for the pulse width ts_1 of the sustaining pulse before the last pulse.

$$(ts_1 + 2) \leq ts_2 \leq 20 \quad (\mu s \text{ of units})$$

7. The plasma display apparatus of claim 3 further comprising:

15 an erasing period after the sustaining period,

 wherein the erasing period is a period when a ramp voltage pulse whose polarity differs from polarity of the last pulse of the sustaining pulse in the sustaining period is applied to an electrode, which differs from an electrode where the last pulse of the sustaining pulse is applied.

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8. The plasma display apparatus of claim 5 further comprising:

an erasing period after the sustaining period,

 wherein the erasing period is a period when a ramp voltage pulse whose polarity differs from polarity of the last pulse of the sustaining pulse in
25 the sustaining period is applied to an electrode, which differs from an electrode where the last pulse of the sustaining pulse is applied.

9. The plasma display apparatus of claim 1,

wherein a slope of the ramp voltage pulse in the erasing period ranges from 0.5 V/ μ s to 20 V/ μ s.

5 10. The plasma display apparatus of claim 3 further comprising:

an erasing period when a ramp voltage pulse whose polarity differs from polarity of the sustaining pulse is applied to an electrode, which differs from an electrode where the last pulse of the sustaining pulse is applied,

10 wherein a slope of the ramp voltage pulse in the erasing period ranges from 0.5 V/ μ s to 20 V/ μ s.

11. The plasma display apparatus of claim 5 further comprising:

15 an erasing period when a ramp voltage pulse whose polarity differs from polarity of the sustaining pulse is applied to an electrode, which differs from an electrode where the last pulse of the sustaining pulse is applied,

wherein a slope of the ramp voltage pulse in the erasing period ranges from 0.5 V/ μ s to 20 V/ μ s.